

Notice of References Cited	Application/Control No. 10/672,774	Applicant(s)/Patent Under Reexamination CHEN ET AL.	
	Examiner Ryan P. Fiegler	Art Unit 2183	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,279,101	08-2001	Witt et al.	712/215
*	B	US-5,970,241	10-1999	Deao et al.	712/227
	C	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hennessey, John L. and David A. Patterson. Computer Architecture, A Quantitative Approach. Second Edition. Morgan Kaufmann: 1990. Pg 97, 127, 195, 242-244, 247-249, 252, 257, and 279.
	V	Johnson, Mike. Superscalar Microprocessor Design. Prentice Hall: 1991. Pg 169.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.